

## Description

### Features

- SGT LV MOSFET technology
- Excellent Qg\*Ron product(FOM)
- Extremely low on-resistance(Ron)

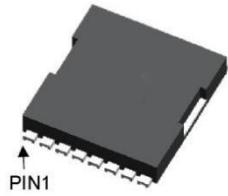
V <sub>DS</sub>	150	V
R <sub>ds(on),typ</sub> @V <sub>gs</sub> =10V	3.3	mΩ
I <sub>D</sub>	250	A

### Application

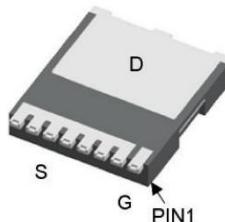
- Battery management
- High current switching
- UPS



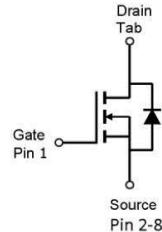
Top View



Bottom View



Pin Configuration



## Package Marking and Ordering Information

Part	Marking	Package	Packing	Reel Size	Tape Width	Qty
HMS250N15LL	HMS250N15LL	Toll-8	Reel	330*28.5mm	24mm	2000pcs

## Key Performance Parameters

Symbol	Parameter	Rating	Units
V <sub>DS</sub>	Drain-Source Voltage	150	V
V <sub>GS</sub>	Gate-Source Voltage	±20	V
I <sub>D</sub> @T <sub>c</sub> =25°C	Continuous Drain Current, V <sub>GS</sub> =10V	250	A
I <sub>D</sub> @T <sub>c</sub> =100°C	Continuous Drain Current, V <sub>GS</sub> =10V	175	A
I <sub>DM</sub>	Pulsed Drain Current	750	A
EAS	Single Pulse Avalanche Energy	1037	mJ
PD@T <sub>c</sub> =25°C	Total Power Dissipation	416	W
T <sub>STG</sub>	Storage Temperature Range	-55 to 150	°C
T <sub>j</sub>	Operating Junction Temperature Range	-55 to 150	°C

## Thermal Data

Symbol	Parameter	Typ	Max	Units
$R_{\theta JA}$	Thermal Resistance Junction-Ambient	42	47	°C/W
$R_{\theta JC}$	Thermal Resistance Junction-Case	0.26	0.3	°C/W

## Electrical Characteristics ( $T_J=25$ °C, Unless otherwise noted)

Symbol	Parameter	Values			Unit	Note / Test Condition
		Min.	Typ.	Max.		
BVDSS	Drain-Source Breakdown Voltage	150	--	--	V	$V_{GS}=0V, I_D=250\mu A$
$R_{DS(ON)}$	Static Drain-Source On-Resistance	--	3.3	4	$m\Omega$	$V_{GS}=10V, I_D=50A$
$V_{GS(th)}$	Gate Threshold Voltage	2	3	4	V	$V_{GS}=V_{DS}, I_D=250\mu A$
IDSS	Drain-Source Leakage Current	--	--	1	$\mu A$	$V_{DS}=150V, V_{GS}=0V, T_J=25^{\circ}C$
		--	--	10	$\mu A$	$V_{DS}=120V, V_{GS}=0V, T_J=125^{\circ}C$
IGSS	Gate-Source Leakage Current	--	--	$\pm 100$	nA	$V_{GS}=\pm 20V, V_{DS}=0V$
$g_{fs}$	Forward Transconductance	--	96	--	S	$V_{DS}=10V, I_D=50A$
$R_g$	Gate Resistance	--	5	--	$\Omega$	$V_{DS}=0V, V_{GS}=0V, f=1MHz$
$Q_g$	Total Gate Charge	--	115	--	nC	$V_{DS}=75V, V_{GS}=10V, I_D=50A$
$Q_{gs}$	Gate-Source Charge	--	41	--		
$Q_{gd}$	Gate-Drain Charge	--	26	--		
$T_{d(on)}$	Turn-On Delay Time	--	52	--		
$T_r$	Rise Time	--	91	--	ns	$V_{DD}=75V, V_{GS}=10V, R_G=3\Omega, I_D=50A$
$T_{d(off)}$	Turn-Off Delay Time	--	92	--		
$T_f$	Fall Time	--	63	--		
$C_{iss}$	Input Capacitance	--	8090	--	pF	$V_{DS}=75V, V_{GS}=0V, f=1MHz$
$C_{oss}$	Output Capacitance	--	806	--		
$C_{rss}$	Reverse Transfer Capacitance	--	29	--		

## Diode Characteristics

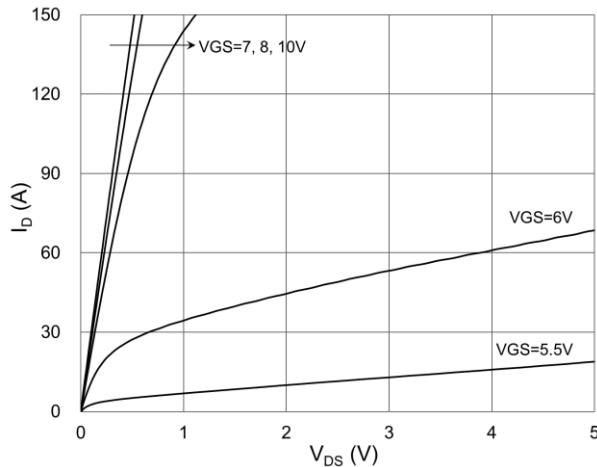
Symbol	Parameter	Conditions	Min	Typ	Max	Units
Trr	Body Diode Reverse Recovery Time	IF=50A, di/dt=100A/us	--	145	--	ns
Qrr	Body Diode Reverse Recovery Charge		--	280	--	nC
$V_{SD}$	Diode Forward Voltage	$V_{GS}=0V, I_S=30A, T_J=25^{\circ}C$		--	1.2	V

Note:

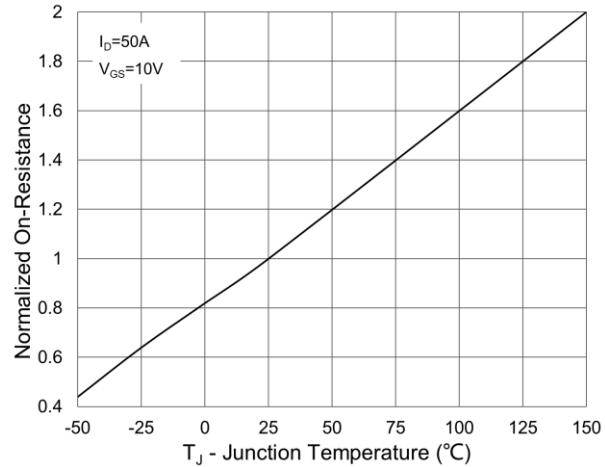
1. Repetitive Rating: Pulsed width limited by maximum junction temperature.
2. The data tested by pulsed, pulse width  $\leq 300\mu s$ , duty cycle  $\leq 2\%$ .
3. Essentially independent of operating temperature.
4. The EAS data shows Max. rating. The test condition is  $V_{DD}=75V, V_{GS}=10V, L=0.5mH$ .

## Typical Performance Characteristics

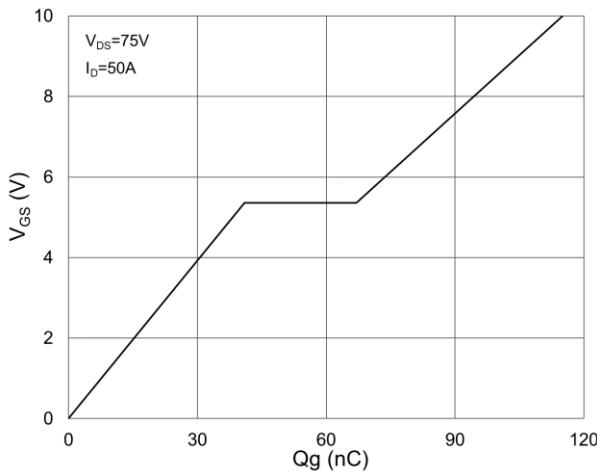
**Fig1 Output Characteristics**



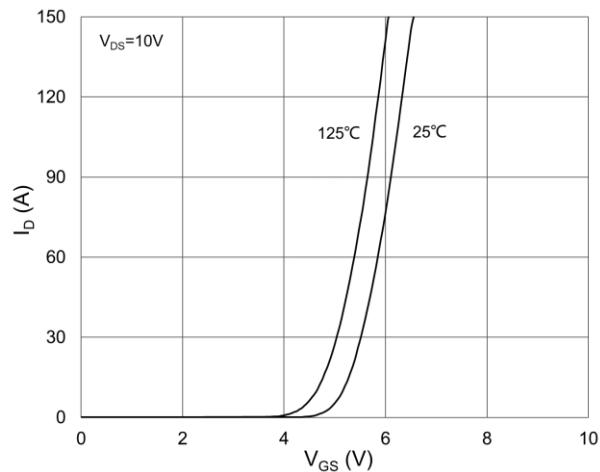
**Fig2 Normalized R<sub>DSON</sub> vs. T<sub>J</sub>**



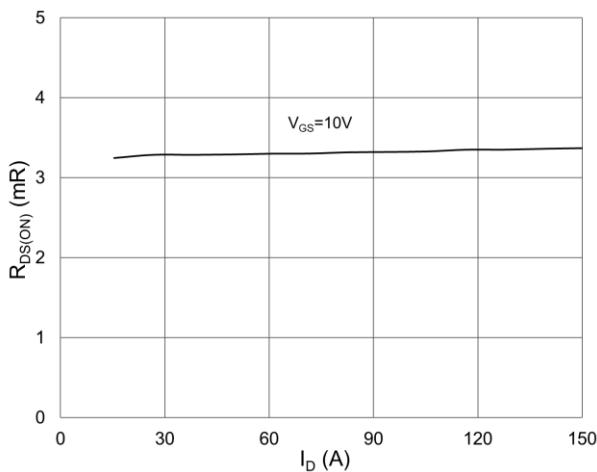
**Fig3 Gate Charge Waveform**



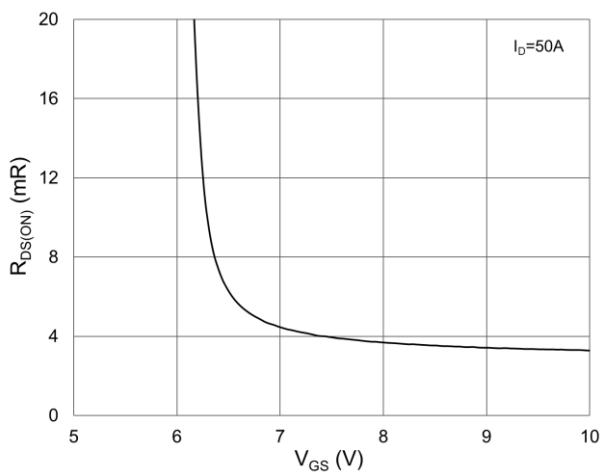
**Fig4 Transfer Characteristics**



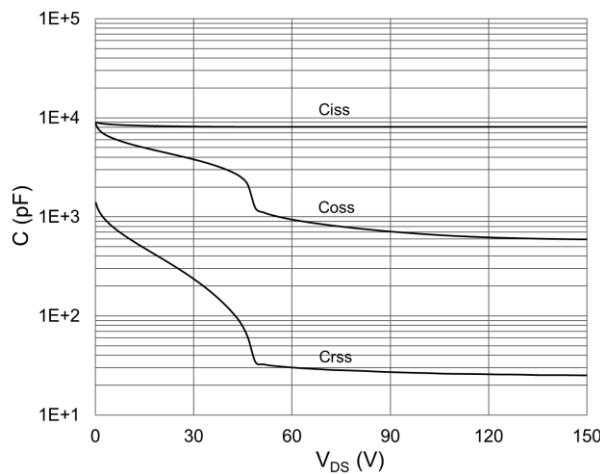
**Fig5 R<sub>d</sub>s(on) vs. Drain Current and Gate Voltage**



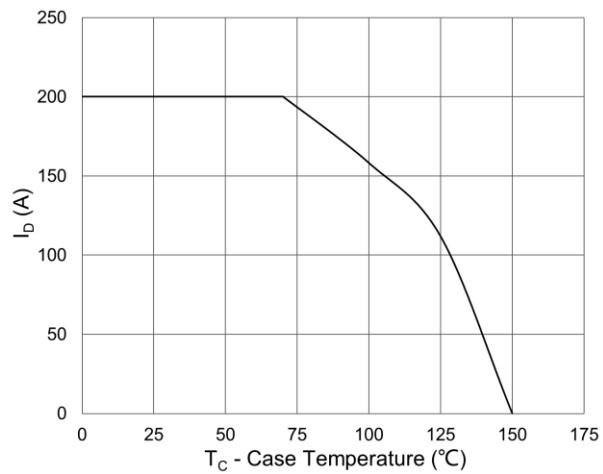
**Fig6 R<sub>d</sub>s(on) vs. Gate Voltage**



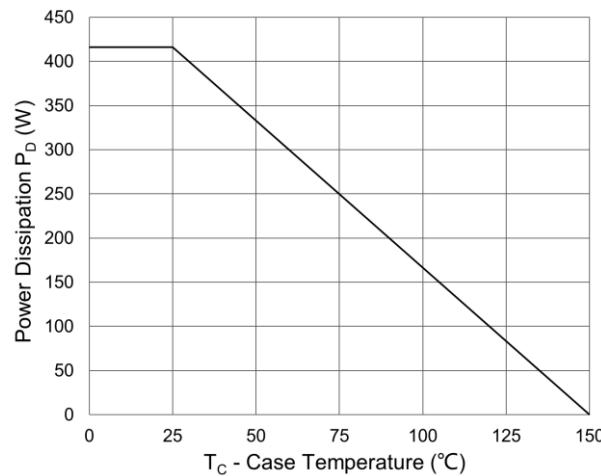
**Fig7 Capacitance Characteristics**



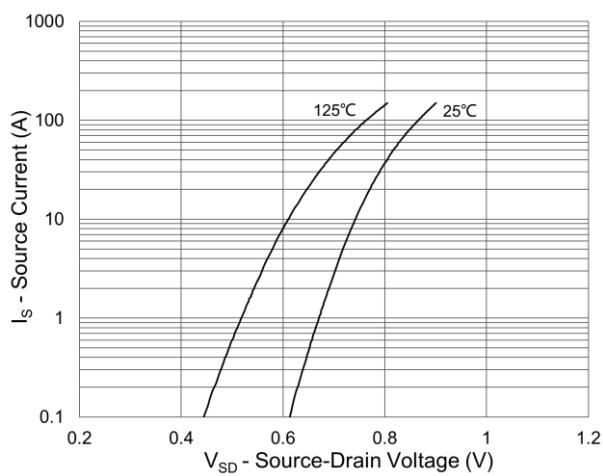
**Fig8 Drain Current Derating**



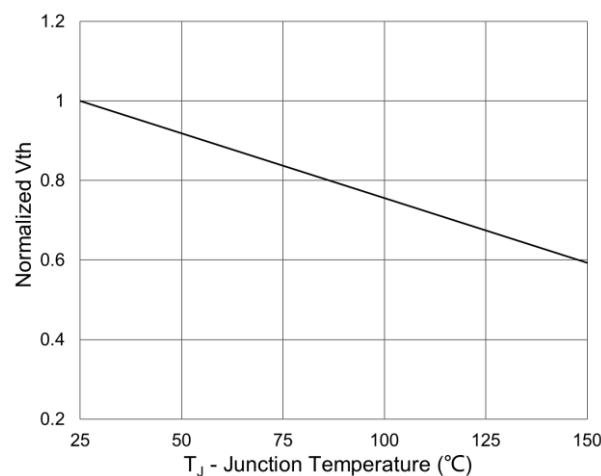
**Fig9 Power Dissipation**



**Fig10 Source-Drain Diode Forward Characteristics**



**Fig11 Normalized Threshold Voltage vs.  $T_J$**



**Fig12 Normalized Breakdown Voltage vs.  $T_J$**

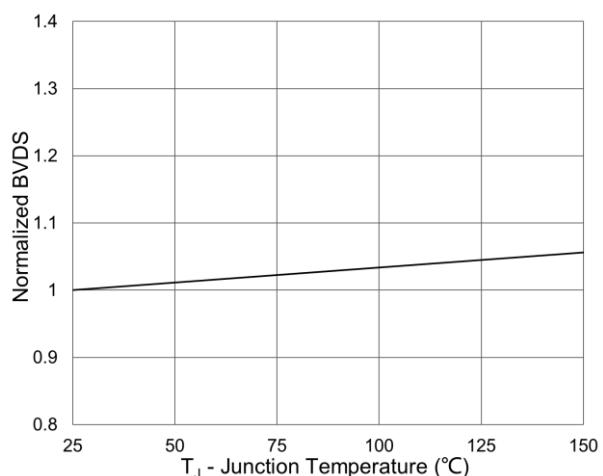


Fig13 Maximum Safe Operation Area

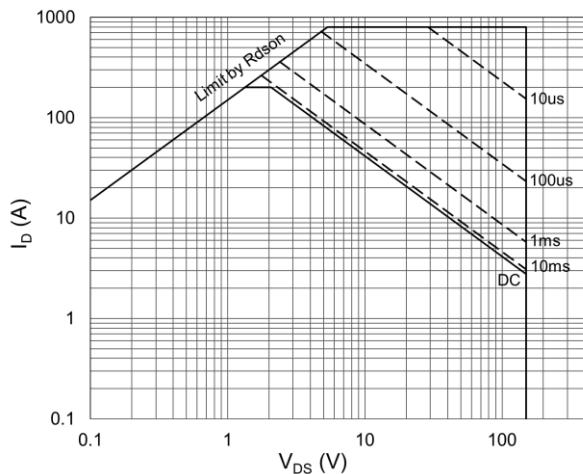
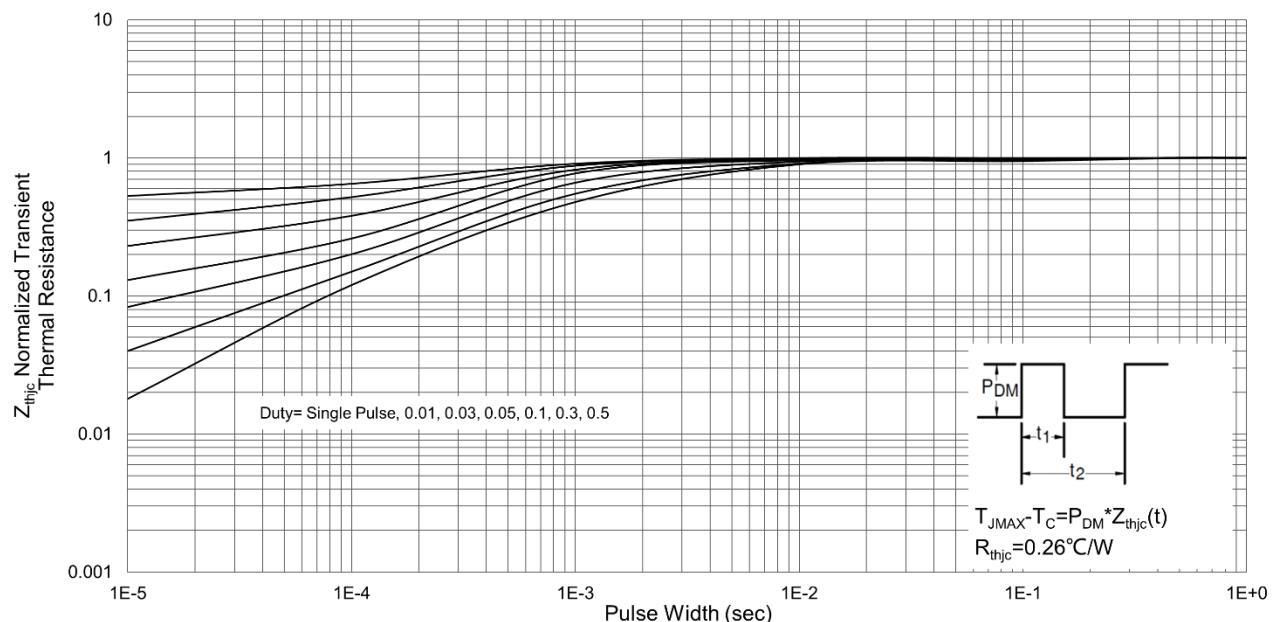
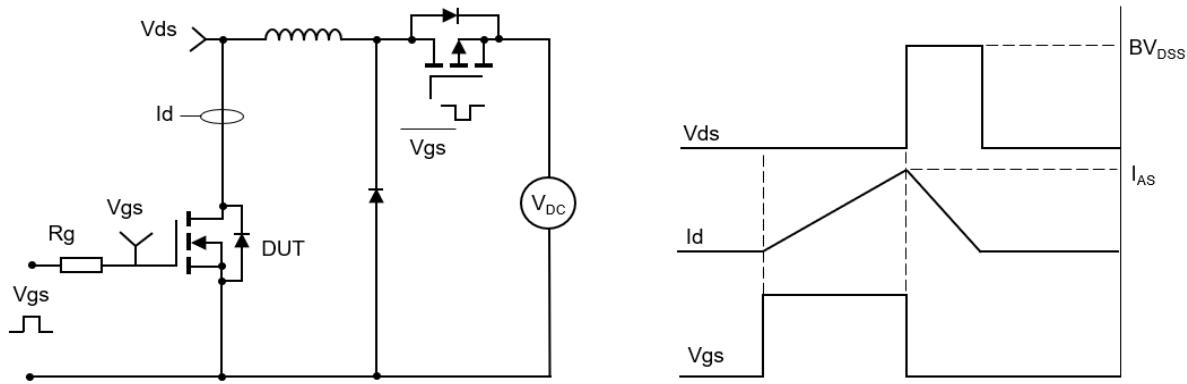


Fig14 Normalized Transient Impedance

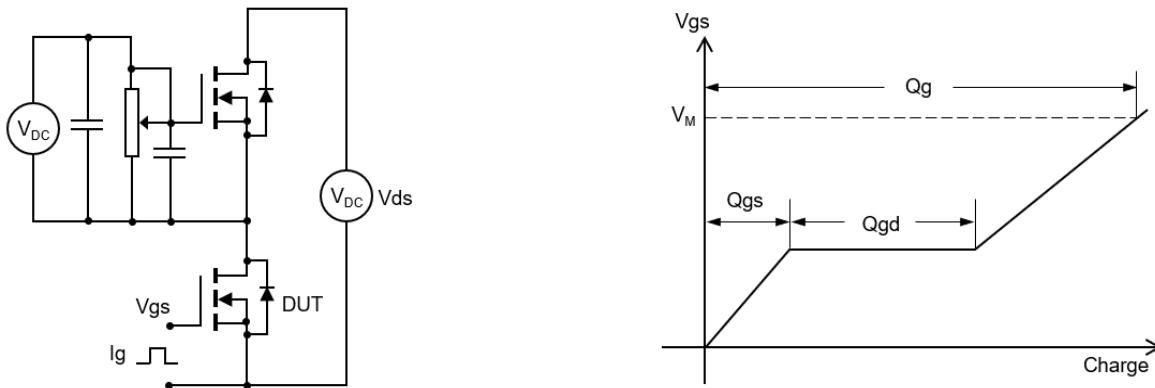


## Test Circuit & Waveform

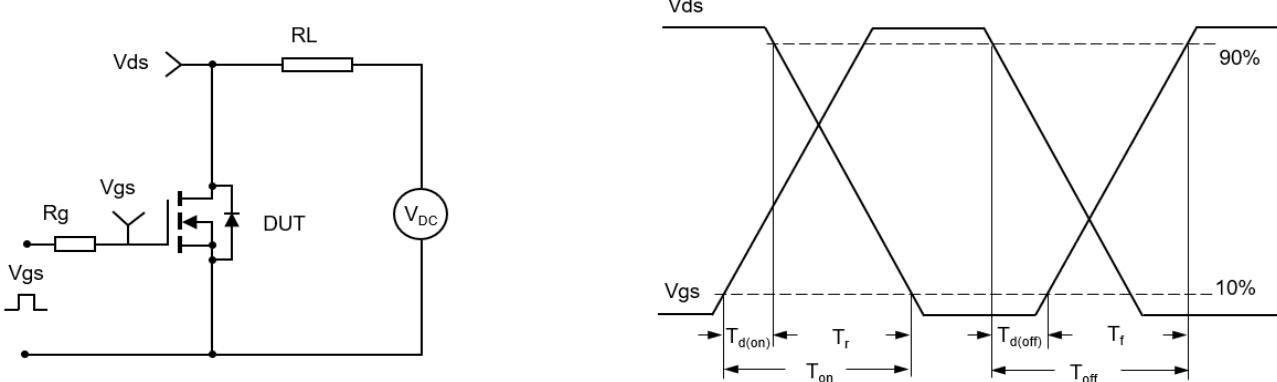
### 1. Unclamped Inductive Switching Test Circuit & Waveform



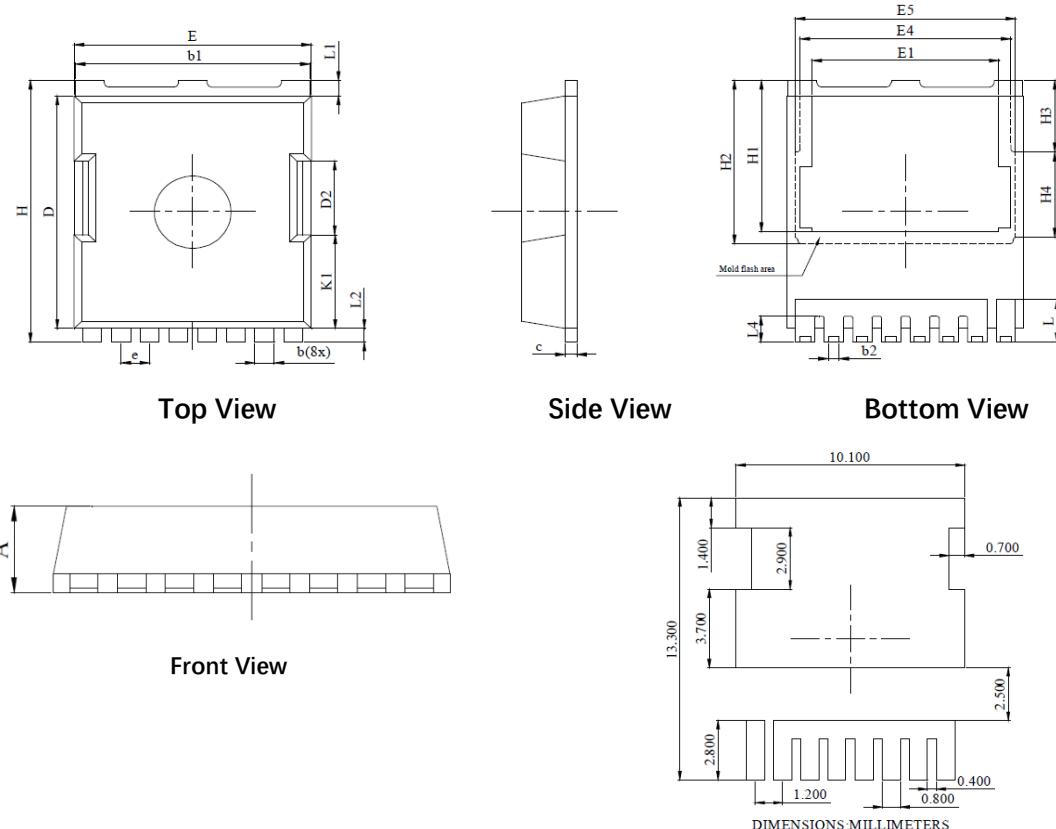
### 2. Gate Charge Test Circuit & Waveform



### 3. Resistive Switching Test Circuit & Waveform



## Toll-8 Package Information



DIM.	MILLIMETER		
	MIN.	NOM.	MAX.
A	2.20	2.30	2.40
b	0.70	0.80	0.90
b1	9.70	9.80	9.90
b2	0.42	0.46	0.50
c	0.40	0.50	0.60
D	10.28	10.38	10.58
D2		3.30	
E	9.70	9.90	10.10
E1		7.80	
E4		8.80	
E5		9.20	
e	1.20 (BSC)		
H	11.48	11.68	11.88
H1	6.55	6.75	6.85
H2		7.30	
H3		3.20	
H4		3.80	
K1		4.18	
L	1.70	1.90	2.10
L1		0.70	
L2		0.60	
L4	1.00	1.15	1.30